

TPA3128D2 Evaluation Module

This user's guide describes the operation of the TPA3128D2EVM. The document also provides design information such as schematic, BOM, and PCB layout.

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1 Hardware Overview

The TPA3128D2 EVM customer evaluation module demonstrates the TPA3128D2 integrated circuit from Texas Instruments (TI). The TPA3128D2 is a 30-W (per channel), efficient stereo digital amplifier power stage for driving 2 bridge-tied speakers or a single parallel bridge-tied load. This document covers EVM specifications and design documentation.

Figure 1 illustrates the EVM board. Table 1 lists EVM specifications.

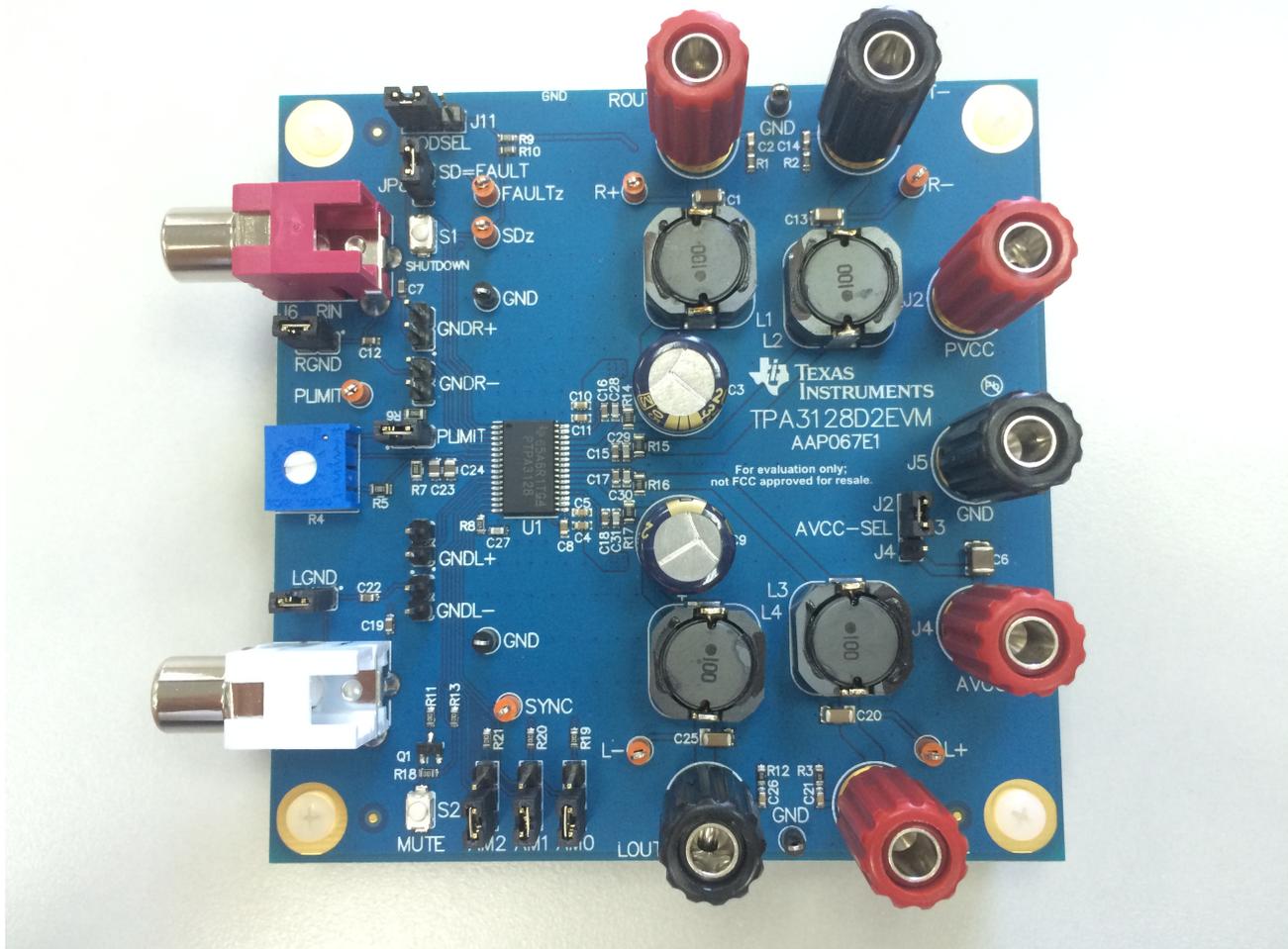


Figure 1. TPA3128D2EVM Board

Table 1. TPA3128D2EVM Specification

Key Parameters	Value
Power supply voltage	4.5 V to 26 V
Number of channels	2 bridge-tied load (BTL) Stereo
Load impedance	3.2 Ω to 8 Ω
Output power BTL	30 W per channel
Output power PBTL	60 W

2 Operation

This section describes the TPA3128D2EVM board in regards to the power supply and system interfaces. Information regarding handling and unpacking, absolute operating conditions, and a description of the factory default switch and jumper configurations is provided.

The following is a step-by-step guide to configuring the TPA3128D2EVM for device evaluation:

2.1 Electrostatic Discharge Warning

Many of the components on the TPA3128D2EVM are susceptible to damage by electrostatic discharge

CAUTION

Failure to observe ESD handling procedures may result in damage to EVM components.

2.2 Unpacking the EVM

On opening the TPA3128D2EVM package, ensure that the following items are included:

- 1 piece TPA3128D2EVM board using one TPA3128D2 device

If the EVM board is missing, contact the Texas Instruments *Product Information Center* nearest you to inquire about a replacement.

2.3 Power Supply Setup

A single power supply or separate PVCC/AVCC is required to power up the EVM. Since most of the pins are PVCC compliant, the PVCC supply can also be used to power the analog supply (AVCC) and to pull up the logic pins for shutdown (SD) control, and fault detection (FAULT).

Table 2. Power Requirements

Description	Voltage Range	Current Requirement	Minimum Wire Size
PVCC/AVCC	4.5 to 26 V	8 A	24 AWG

CAUTION

Applying voltages above the limitations given in [Table 2](#) may cause permanent damage to your hardware.

2.4 Evaluation Module Preparations

Prepare the EVM using the following steps:

1. Ensure that the external power source is set to OFF.
2. Connect the external regulated power supply adjusted from 4.5 V to 26 V to the module PVCC and GND banana jacks taking care to observe marked polarity.

2.5 Inputs and Outputs

Make the following input and output connections:

1. Connect audio inputs, either differential or single-ended, to the LIN and RIN RCA phono plugs Stereo output operation. To enable the individual channel shutdown feature, tie RINP and RINN directly to GND (without capacitors) and OUTPL and OUTNL should be working normally in BTL configuration. For PBTL operation, apply a single input, differential or single-ended, to the RIN RCA phono plug and tie LINP and LINN directly to GND (without capacitors).
2. For a BTL configuration, connect a load across the outputs (OUTL+ and OUTL-) and (OUTR+ and OUTR-). For PBTL configuration, connect a single load from one of the left speaker jacks to one of the right speaker jacks, depending on how the filters are loaded.

2.6 Power Up

When powering up the EVM, use the following steps:

1. Verify the correct voltage and input polarity and turn the external power supplies ON. The EVM should begin operation.
2. Adjust the audio source for the correct volume.

2.7 Recommended Settings

This section contains the recommended settings for stereo, single-channel, and mono output.

[Table 3](#) lists the settings for a single-ended input with auto recovery, Plimit, and BTL (stereo output).

Table 3. TPA3128D2EVM Setting 1

Setting	Description
J6 (RIN)	R channel audio input
J9 (LIN)	L channel audio input
JP1 (GNDR+) and JP3 (GNDR-), Open	R channel on
JP4 (GNDL+) and JP6 (GNDL-), Open	L channel on
JP2 (RGND), Installed	Single-ended input (R channel)
JP5 (LGND), Installed	Single-ended input (L channel)
JP7, Installed	Set PLIMIT
R4	Change PLIMIT voltage value
JP8 ($\overline{SD} = \text{FAULT}$), Installed	Auto-recovery mode
J11 (MODESEL = L)	Low idle-loss mode

[Table 4](#) lists the settings for a single-ended input with auto recovery, Plimit, and BTL (single-channel output).

Table 4. TPA3128D2EVM Setting 2

Setting	Description
J6 (RIN)	No audio input
J9 (LIN)	L channel audio input
JP1 (GNDR+) and JP3 (GNDR-), Installed	R channel off
JP4 (GNDL+) and JP6 (GNDL-), Open	L channel on
JP5 (LGND), Installed	Single-ended input (L channel)
JP7, Installed	Set PLIMIT
R4	Change PLIMIT voltage value
JP8 ($\overline{SD} = \text{FAULT}$), Installed	Auto-recovery mode
J11 (MODESEL = L)	Low idle-loss mode

Table 5 lists the settings for a single-ended input with auto recovery, Plimit, and PBTL (mono output).

Table 5. TPA3128D2EVM Setting 3

Setting	Description
J6 (RIN)	R channel audio input
J9 (LIN)	No audio input
JP1 (GNDR+) and JP3 (GNDR-), Open	R channel on
JP4 (GNDL+) and JP6 (GNDL-), Installed	L channel off, PBTL mode
JP2, Installed	Single-ended input (R channel)
JP7, Installed	Set PLIMIT
R4	Change PLIMIT voltage value
JP8 ($\overline{SD} = \text{FAULT}$), Installed	Auto-recovery mode
J11 (MODESEL = L)	Low idle-loss mode

2.8 Separate PVCC/AVCC Supply Support

By supplying a voltage of AVCC which is lower than PVCC (for example, PVCC = 24 V and AVCC = 12 V), the power dissipation on the internal LDO is reduced. This gives customers the flexibility to choose between extreme low-idle current mode (J3, 1 and 2 set to Short, with separate AVCC) and single power supply mode (J3, 2 and 3 set to Short, AVCC = PVCC).

3 Board Layouts, Bill of Materials, and Schematic

3.1 TPA3128D2EVM Board Layouts

Figure 2 and Figure 3 illustrate the board layouts for the EVM.

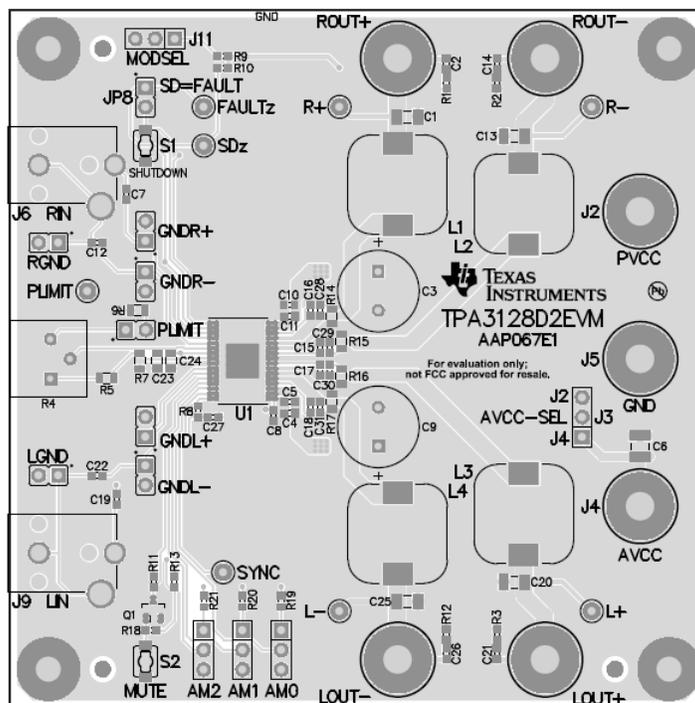


Figure 2. TPA3128D2EVM Top Overlay

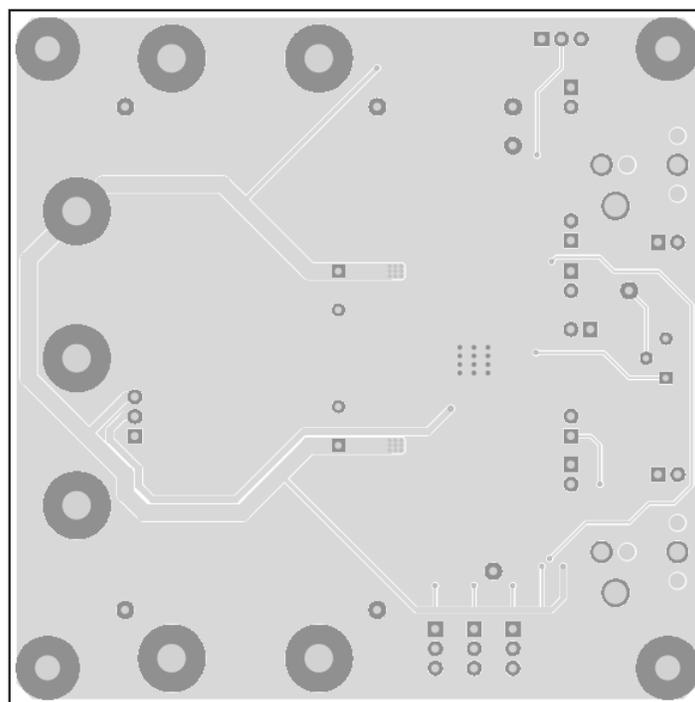


Figure 3. TPA3128D2EVM Bottom Overlay

3.2 Bill of Materials

Table 6 displays the BOM for this EVM.

Table 6. TPA3128D2EVM Parts List

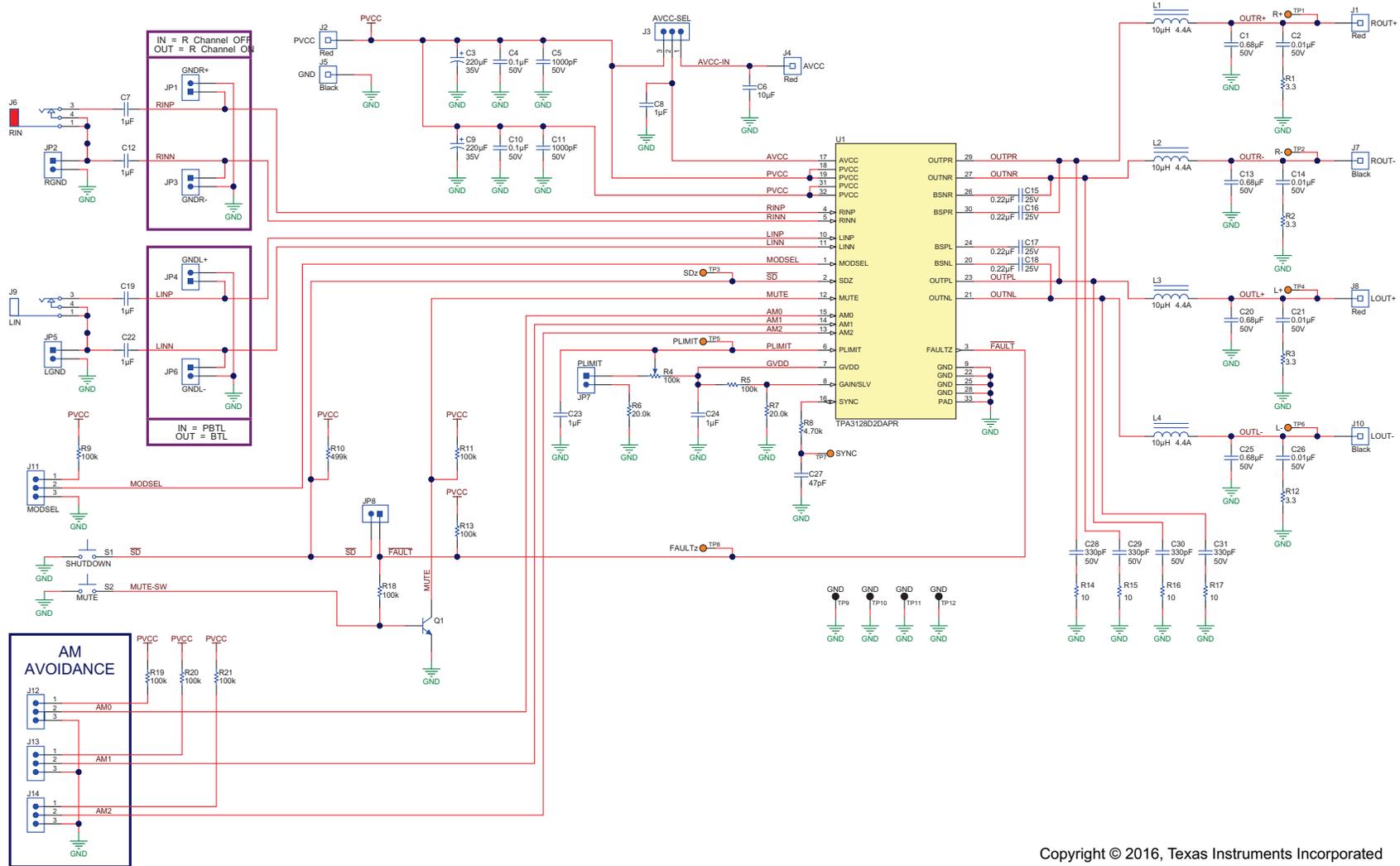
Designator	Qty	Value	Description	Package Reference	Part Number	Manufacturer	Alternate Part Number	Alternate Manufacturer
IPCB1	1		Printed Circuit Board		AAP067	Any	-	-
C1, C13, C20, C25	4	0.68uF	CAP, CERM, 0.68 µF, 50 V, +/- 10%, X7R, 1206	1206	GRM31MR71H684KA88L	Murata		
C2, C14, C21, C26	4	0.01uF	CAP, CERM, 0.01 µF, 50 V, +/- 10%, X7R, 0603	0603	GRM188R71H103KA01D	Murata		
C3, C9	2	220uF	CAP, AL, 220 µF, 35 V, +/- 20%, 0.09 ohm, TH	10x12.5mm	EEU-FC1V221	Panasonic		
C4, C10	2	0.1uF	CAP, CERM, 0.1 µF, 50 V, +/- 10%, X7R, 0603	0603	GCM188R71H104KA57D	Murata		
C5, C11	2	1000pF	CAP, CERM, 1000 pF, 50 V, +/- 5%, C0G/NP0, 0603	0603	GRM1885C1H102JA01D	Murata		
C6	1	10uF	CAP, CERM, 10 µF, 35 V, +/- 10%, X7R, 1210	1210	GRM32ER7YA106KA12L	Murata		
C7, C12, C19, C22	4	1uF	CAP, CERM, 1 µF, 16 V, +/- 10%, X7R, 0603	0603	GRM188R71C105KA12D	Murata		
C8	1	1uF	CAP, CERM, 1 µF, 30 V, +/- 10%, X7R, 0603	0603	C1608X7R1V105K080AC	TDK		
C15, C16, C17, C18	4	0.22uF	CAP, CERM, 0.22 µF, 25 V, +/- 10%, X7R, 0603	0603	GRM188R71E224KA88D	Murata		
C23, C24	2	1uF	CAP, CERM, 1 µF, 16 V, +/- 10%, X7R, 0805	0805	C0805C105K4RACTU	Kemet		
C27	1	47pF	CAP, CERM, 47 pF, 50 V, +/- 5%, C0G/NP0, 0603	0603	GRM1885C1H470JA01D	Murata		
C28, C29, C30, C31	4	330pF	CAP, CERM, 330 pF, 50 V, +/- 5%, C0G/NP0, 0603	0603	GRM1885C1H331JA01D	Murata		
H1, H2, H3, H4	4		Machine Screw, Round, #4-40 x 1/4, Nylon, Philips panhead	Screw	NY PMS 440 0025 PH	B&F Fastener Supply		
H5, H6, H7, H8	4		Standoff, Hex, 0.5"L #4-40 Nylon	Standoff	1902C	Keystone	-	-
J1, J2, J4, J8	4		Binding Post, RED, TH	11.4x27.2mm	7006	Keystone		
J3, J11, J12, J13, J14	5		Header, 100mil, 3x1, Gold, TH	PBC03SAA N	PBC03SAAN	Sullins Connector Solutions		
J5, J7, J10	3		Binding Post, BLACK, TH	11.4x27.2mm	7007	Keystone		
J6	1		RCA Jack, Red, R/A, TH	PC Mount Phono Jack-Red, TH	971	Keystone		
J9	1		RCA Jack, White, R/A, TH	PC Mount Phono Jack-White, TH	970	Keystone		
JP1, JP2, JP3, JP4, JP5, JP6, JP7, JP8	8		Header, 100mil, 2x1, Gold, TH	Sullins 100mil, 1x2, 230 mil above insulator	PBC02SAAN	Sullins Connector Solutions		
L1, L2, L3, L4	4	10uH	Inductor, Ferrite, 10 µH, 4.4 A, 23 ohm, SMD	12.8x12.8mm	#B953AS-100M=P3	Murata Toko		
Q1	1	40 V	Transistor, NPN, 40 V, 0.6 A, SOT-23	SOT-23	MMBT2222ALT1G	ON Semiconductor		
R1, R2, R3, R12	4	3.3	RES, 3.3, 5%, 0.1 W, 0603	0603	CRCW06033R30JNEA	Vishay-Dale		

Table 6. TPA3128D2EVM Parts List (continued)

Designator	Qty	Value	Description	Package Reference	Part Number	Manufacturer	Alternate Part Number	Alternate Manufacturer
R4	1	100k	TRIMMER, 100k ohm, 0.5W, TH	375x190x37 5mil	3386P-1-104LF	Bourns		
R5	1	100k	RES, 100 k, 1%, 0.125 W, 0805	0805	CRCW0805100KFKEA	Vishay-Dale		
R6, R7	2	20.0k	RES, 20.0 k, 1%, 0.125 W, 0805	0805	CRCW080520K0FKEA	Vishay-Dale		
R8	1	4.70k	RES, 4.70 k, 0.1%, 0.1 W, 0603	0603	RG1608P-472-B-T5	Susumu Co Ltd		
R9, R11, R13, R18, R19, R20, R21	7	100k	RES, 100k ohm, 1%, 0.1W, 0603	0603	RC0603FR-07100KL	Yageo America		
R10	1	499k	RES, 499 k, 1%, 0.1 W, 0603	0603	RC0603FR-07499KL	Yageo America		
R14, R15, R16, R17	4	10	RES, 10, 5%, 0.125 W, 0805	0805	CRCW080510R0JNEA	Vishay-Dale		
S1, S2	2		Switch, Tactile, SPST-NO, 0.05A, 12V, SMT	Switch, 4.4x2x2.9 mm	TL1015AF160QG	E-Switch		
SH1, SH2, SH3, SH4, SH5, SH6, SH7, SH8, SH9	9	1x2	Shunt, 100mil, Gold plated, Black	Shunt	969102-0000-DA	3M	SNT-100-BK-G	Samtec
TP1, TP2, TP3, TP4, TP5, TP6, TP7, TP8	8	Orange	Test Point, Miniature, Orange, TH	Orange Miniature Testpoint	5003	Keystone		
TP9, TP10, TP11, TP12	4		Test Point, Miniature, Black, TH	Black Miniature Testpoint	5001	Keystone		
U1	1		TPA3128D2, DAP0032A	DAP0032A	TPA3128D2DAPR	Texas Instruments		Texas Instruments
FID1, FID2, FID3	0		Fiducial mark. There is nothing to buy or mount.	Fiducial	N/A	N/A		
Notes:			Unless otherwise noted in the Alternate Part Number and/or Alternate Manufacturer columns, all parts may be substituted with equivalents.					

3.3 TPA3128D2EVM Schematic

Figure 4 illustrates the schematics for the TPA3128D2EVM.



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Figure 4. TPA3128D2EVM Schematic

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3. *Regulatory Notices:*
 - 3.1 *United States*
 - 3.1.1 *Notice applicable to EVMs not FCC-Approved:*

This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.
 - 3.1.2 *For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:*

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

3.3 Japan

3.3.1 *Notice for EVMs delivered in Japan:* Please see http://www.tij.co.jp/lstds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。
http://www.tij.co.jp/lstds/ti_ja/general/eStore/notice_01.page

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If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required by Radio Law of Japan to follow the instructions below with respect to EVMs:

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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4 *EVM Use Restrictions and Warnings:*

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

4.3 *Safety-Related Warnings and Restrictions:*

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4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

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