

# Gate Drive Optocoupler, High Noise Immunity, 2.5 A Output Current

## FOD3120

#### Description

The FOD3120 is a 2.5 A Output Current Gate Drive Optocoupler, capable of driving most medium power IGBT/MOSFET. It is ideally suited for fast switching driving of power IGBT and MOSFETs used in motor control inverter applications, and high performance power system.

It utilizes **onsemi's** coplanar packaging technology, OPTOPLANAR<sup>®</sup>, and optimized IC design to achieve high noise immunity, characterized by high common mode rejection.

It consists of a gallium aluminum arsenide (AlGaAs) light emitting diode optically coupled to an integrated circuit with a high-speed driver for push-pull MOSFET output stage.

#### **Features**

- High Noise Immunity Characterized by 35 kV/μs Minimum Common Mode Rejection
- 2.5 A Peak Output Current Driving Capability for Most 1200 V/20 A IGBT
- Use of P-Channel MOSFETs at Output Stage Enables Output Voltage Swing Close to the Supply Rail
- Wide Supply Voltage Range from 15 V to 30 V
- Fast Switching Speed
  - 400 ns maximum Propagation Delay
  - 100 ns maximum Pulse Width Distortion
- Under Voltage LockOut (UVLO) with Hysteresis
- Extended Industrial Temperate Range,
   -40°C to 100°C Temperature Range
- Safety and Regulatory Approvals
  - UL1577, 5000 V<sub>RMS</sub> for 1 min.
  - ◆ DIN EN/IEC60747-5-5
- $R_{DS(ON)}$  of 1  $\Omega$  (typ.) Offers Lower Power Dissipation
- >8.0 mm Clearance and Creepage Distance (Option 'T' or 'TS')
- 1414 V Peak Working Insulation Voltage (V<sub>IORM</sub>)
- This is a Pb-Free Device

#### **Applications**

- Industrial Inverter
- Uninterruptible Power Supply
- Induction Heating
- Isolated IGBT/Power MOSFET Gate Drive

#### **Related Resources**

- FOD3150, 1 A Output Current, Gate Drive Optocoupler Datasheet
- <a href="https://www.onsemi.com/products/optoelectronics/">https://www.onsemi.com/products/optoelectronics/</a>



PDIP8 GW CASE 709AC



PDIP8 9.655x6.6, 2.54P CASE 646CQ



PDIP8 GW CASE 709AD



PDIP8 6.6x3.81, 2.54P

#### MARKING DIAGRAM

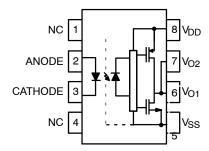


3120 = Device Number

V = DIN\_EN/IEC60747-5-5 Option (only appears on component ordered with this option)

XX = Two Digit Year Code YY = Two Digit Work Week B = Assembly Package Code

#### **FUNCTIONAL BLOCK DIAGRAM**



Note: A 0.1  $\mu$ F bypass capacitor must be connected between pins 5 and 8.

#### **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 14 of this data sheet.

**Table 1. TRUTH TABLE** 

LED	V <sub>DD</sub> - V <sub>SS</sub> "Positive Going" (Turn-on)	V <sub>DD</sub> – V <sub>SS</sub> "Negative Going" (Turn-off)	v <sub>o</sub>
Off	0 V to 30 V	0 V to 30 V	Low
On	0 V to 11.5 V	0 V to 10 V	Low
On	11.5 V to 13.5 V	10 V to 12 V	Transition
On	13.5 V to 30 V	12 V to 30 V	High

#### **Table 2. PIN DEFINITIONS**

Pin #	Name	Description	
1	NC	Not Connected	
2	Anode	LED Anode	
3	Cathode	LED Cathode	
4	NC	Not Connected	
5	V <sub>SS</sub>	Negative Supply Voltage	
6	V <sub>O2</sub>	Output Voltage 2 (internally connected to V <sub>O1</sub> )	
7	V <sub>O1</sub>	Output Voltage 1	
8	V <sub>DD</sub>	Positive Supply Voltage	

#### **Table 3. SAFETY AND INSULATION RATINGS**

As per DIN EN/IEC 60747–5–5. This optocoupler is suitable for "safe electrical insulation" only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.

Symbol	Parameter		Min.	Тур.	Max.	Unit
	Installation Classifications per DIN VDE	< 150 V <sub>RMS</sub>		I–IV		
	0110/1.89 Table 1, For Rated Mains Voltage	< 300 V <sub>RMS</sub>		I–IV		1
		< 450 V <sub>RMS</sub>		I–III		1
		< 600 V <sub>RMS</sub>		I–III		1
		< 1000 V <sub>RMS</sub> (Option T, TS)		I–III		
	Climatic Classification	-		40/100/21		
	Pollution Degree (DIN VDE 0110/1.89)			2		
CTI	Comparative Tracking Index		175			
$V_{PR}$	Input to Output Test Voltage, Method A, $V_{IORM} \times 1.6 = V_{PR}$ , Type and Sample Test with $t_m = 10$ s, Partial Discharge < 5 pC		2262			Vpeak
	Input to Output Test Voltage, Method B, $V_{IORM} \times 1.87$ 100% Production Test with $t_m = 1$ s, Partial Discharg		2651			Vpeak
V <sub>IORM</sub>	Maximum Working Insulation Voltage		1414			Vpeak
V <sub>IOTM</sub>	Highest Allowable Over Voltage		6000			Vpeak
	External Creepage		8.0			mm
	External Clearance		7.4			mm
	External Clearance (for Option T or TS, 0.4" Lead Sp	acing)	10.16			mm
DTI	Distance Through Insulation (Insulation Thickness)		0.5			mm
T <sub>S</sub>	Case Temperature (Note 1)		175			°C
I <sub>S,INPUT</sub>	Input Current (Note 1)		400			mA
P <sub>S,OUTPUT</sub>	Output Power (Duty Factor ≤ 2.7 %) (Note 1)		700			mW
R <sub>IO</sub>	Insulation Resistance at T <sub>S</sub> , V <sub>IO</sub> = 500 V (Note 1)		10 <sup>9</sup>			Ω

<sup>1.</sup> Safety limit value – maximum values allowed in the event of a failure.

Table 4. ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25°C unless otherwise specified.)

Symbol	Parameter	Value	Units
T <sub>STG</sub>	Storage Temperature	-55 to +125	°C
T <sub>OPR</sub>	Operating Temperature	-40 to +100	°C
$T_J$	Junction Temperature	-40 to +125	°C
T <sub>SOL</sub>	Lead Wave Solder Temperature (refer to page 13 for reflow solder profile)	260 for 10 s	°C
I <sub>F(AVG)</sub>	Average Input Current	25	mA
I <sub>F(Peak)</sub>	Peak Transient Forward Current (Note 2)	1	А
f	Operating Frequency (Note 3)	50	kHz
V <sub>R</sub>	Reverse Input Voltage	5	V
I <sub>O(PEAK)</sub>	Peak Output Current (Note 4)	3.0	А
$V_{DD} - V_{SS}$	Supply Voltage	0 to 35	V
	T <sub>A</sub> ≥ 90°C	0 to 30	
V <sub>O(PEAK)</sub>	Peak Output Voltage	0 to V <sub>DD</sub>	V
$t_{R(IN)}, t_{F(IN)}$	Input Signal Rise and Fall Time	500	ns
PDI	Input Power Dissipation (Note 5, Note 7)	45	mW
PDo	Output Power Dissipation (Note 6, Note 7)	250	mW

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 2. Pulse Width, PW  $\leq$  1  $\mu$ s, 300 pps 3. Exponential Waveform,  $I_{O(PEAK)} \leq |2.5 \text{ A}| \ (\leq 0.3 \ \mu\text{s})$ 4. Maximum pulse width = 10  $\mu$ s, maximum duty cycle = 1.1% 5. Derate linearly above 87°C, free air temperature at a rate of 0.77 mW/°C
- 6. No derating required across temperature range.
- 7. Functional operation under these conditions is not implied. Permanent damage may occur if the device is subjected to conditions outside these ratings.

#### **Table 5. RECOMMENDED OPERATING CONDITIONS**

Symbol	Parameter	Value	Units
T <sub>A</sub>	Ambient Operating Temperature	-40 to +100	°C
$V_{DD} - V_{SS}$	Power Supply	15 to 30	V
I <sub>F(ON)</sub>	Input Current (ON)	7 to 16	mA
V <sub>F(OFF)</sub>	Input Voltage (OFF)	0 to 0.8	V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

#### **Table 6. ISOLATION CHARACTERISTICS**

Apply over all recommended conditions, typical value is measured at T<sub>A</sub> = 25°C

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
V <sub>ISO</sub>	Input-Output Isolation Voltage	$T_A$ = 25°C, R.H.< 50 %, t = 1.0 min., $I_{I-O} \le$ 10 $\mu$ A, 50 Hz (Note 8, Note 9)	5000			V <sub>RMS</sub>
R <sub>ISO</sub>	Isolation Resistance	V <sub>I-O</sub> = 500 V (Note 8)		10 <sup>11</sup>		Ω
C <sub>ISO</sub>	Isolation Capacitance	V <sub>I-O</sub> = 0 V, Frequency = 1.0 MHz (Note 8)		1		pF

<sup>8.</sup> Device is considered a two terminal device: pins 2 and 3 are shorted together and pins 5, 6, 7 and 8 are shorted together.

<sup>9. 5000</sup>  $V_{\text{RMS}} \, \text{for 1} \, \text{minute duration is equivalent to 6000 VAC}_{\text{RMS}} \, \text{for 1 second duration.}$ 

#### **Table 7. ELECTRICAL CHARACTERISTICS**

Apply over all recommended conditions, typical value is measured at  $V_{DD} = 30 \text{ V}$ ,  $V_{SS} = \text{Ground}$ ,  $T_A = 25^{\circ}\text{C}$  unless otherwise specified.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
V <sub>F</sub>	Input Forward Voltage	I <sub>F</sub> = 10 mA	1.2	1.5	1.8	V
$\Delta$ (V <sub>F</sub> /T <sub>A</sub> )	Temperature Coefficient of Forward Voltage			-1.8		mV/°C
BV <sub>R</sub>	Input Reverse Breakdown Voltage	I <sub>R</sub> = 10 μA	5			V
C <sub>IN</sub>	Input Capacitance	f = 1 MHz, V <sub>F</sub> = 0 V		60		pF
I <sub>OH</sub>	High Level Output Current	$V_O = V_{DD} - 3 V$	-1.0	-2.0	-2.5	Α
	(Note 3)	$V_O = V_{DD} - 6 V$	-2.0		-2.5	
I <sub>OL</sub>	Low Level Output Current	$V_0 = V_{SS} + 3 V$	1.0	2.0	2.5	Α
	(Note 3)	$V_0 = V_{SS} + 6 V$	2.0		2.5	
V <sub>OH</sub>	High Level Output Voltage	$I_F = 10 \text{ mA}, I_O = -2.5 \text{ A}$	$V_{DD}-6.25~V$	$V_{DD} - 2.5 V$		V
		$I_F = 10 \text{ mA}, I_O = -100 \text{ mA}$	V <sub>DD</sub> – 0.25 V	V <sub>DD</sub> – 0.1 V		
V <sub>OL</sub>	Low Level Output Voltage	$I_F = 0 \text{ mA}, I_O = 2.5 \text{ A}$		V <sub>SS</sub> + 2.5 V	V <sub>SS</sub> + 6.25 V	V
		$I_F = 0 \text{ mA}, I_O = 100 \text{ mA}$		V <sub>SS</sub> + 0.1 V	V <sub>SS</sub> + 0.25 V	
I <sub>DDH</sub>	High Level Supply Current	$V_O$ = Open, $I_F$ = 7 to 16 mA		2.8	3.8	mA
I <sub>DDL</sub>	Low Level Supply Current	$V_O = Open, V_F = 0 to 0.8 V$		2.8	3.8	mA
I <sub>FLH</sub>	Threshold Input Current Low to High	$I_{O} = 0 \text{ mA}, V_{O} > 5 \text{ V}$		2.3	5.0	mA
V <sub>FHL</sub>	Threshold Input Voltage High to Low	I <sub>O</sub> = 0 mA, V <sub>O</sub> < 5 V	0.8			V
V <sub>UVLO+</sub>	Under Voltage Lockout	I <sub>F</sub> = 10mA, V <sub>O</sub> > 5 V	11.5	12.7	13.5	V
V <sub>UVLO</sub> _	Threshold	I <sub>F</sub> = 10 mA, V <sub>O</sub> < 5 V	10.0	11.2	12.0	V
UVLO <sub>HYS</sub>	Under Voltage Lockout Threshold Hysteresis			1.5		V

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

#### **Table 8. SWITCHING CHARACTERISTICS**

Apply over all recommended conditions, typical value is measured at  $V_{DD}$  = 30 V,  $V_{SS}$  = Ground,  $T_A$  = 25°C unless otherwise specified.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
t <sub>PHL</sub>	Propagation Delay Time to Logic Low Output	I <sub>F</sub> = 7 mA to 16 mA,	150	275	400	ns
t <sub>PLH</sub>	Propagation Delay Time to Logic High Output	$\dot{R}g$ = 10 $\Omega$ , $\dot{C}g$ = 10 nF, f = 10 kHz, Duty Cycle = 50 %	150	255	400	ns
PWD	Pulse Width Distortion,   t <sub>PHL</sub> - t <sub>PLH</sub>			20	100	ns
PDD (Skew)	Propagation Delay Difference Between Any Two Parts or Channels, (t <sub>PHL</sub> - t <sub>PLH</sub> ) (Note 10)		-250		250	ns
t <sub>R</sub>	Output Rise Time (10% – 90%)			60		ns
t <sub>F</sub>	Output Fall Time (90% – 10%)			60		ns
t <sub>UVLO ON</sub>	UVLO Turn On Delay	$I_F = 10 \text{ mA} , V_O > 5 \text{ V}$		1.6		μs
t <sub>UVLO OFF</sub>	UVLO Turn Off Delay	$I_F = 10 \text{ mA}$ , $V_O < 5 \text{ V}$		0.4		μs
CM <sub>H</sub>	Common Mode Transient Immunity at Output High	$T_A = 25^{\circ}\text{C}, V_{DD} = 30 \text{ V},$ $I_F = 7 \text{ to } 16 \text{ mA}, V_{CM} = 2000 \text{ V}$ (Note 11)	35	50		kV/μs
CM <sub>L</sub>	Common Mode Transient Immunity at Output Low	$T_A = 25^{\circ}C$ , $V_{DD} = 30$ V, $V_F = 0$ V, $V_{CM} = 2000$ V (Note 12)	35	50		kV/μs

<sup>10.</sup> The difference between  $t_{\text{PHL}}$  and  $t_{\text{PLH}}$  between any two FOD3120 parts under same test conditions.

<sup>11.</sup> Common mode transient immunity at output high is the maximum tolerable negative dVcm/dt on the trailing edge of the common mode

impulse signal, Vcm, to assure that the output will remain high (i.e., V<sub>O</sub> > 15.0 V).

12. Common mode transient immunity at output low is the maximum tolerable positive dVcm/dt on the leading edge of the common pulse signal, Vcm, to assure that the output will remain low (i.e., V<sub>O</sub> < 1.0 V).

#### TYPICAL PERFORMANCE CHARACTERISTICS

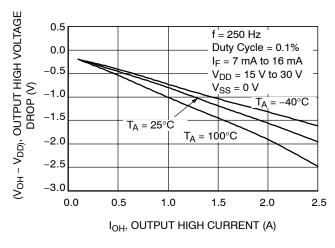


Figure 1. Output High Voltage Drop vs. Output High Current

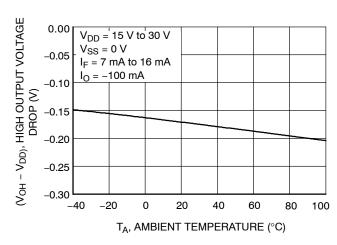


Figure 2. Output High Voltage Drop vs. Ambient Temperature

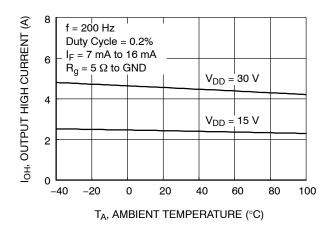


Figure 3. Output High Current vs. Ambient Temperature

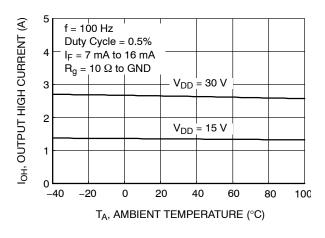


Figure 4. Output High Current vs. Ambient Temperature

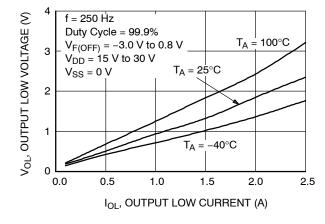


Figure 5. Output Low Voltage vs. Output Low Current

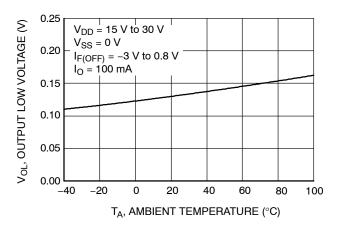


Figure 6. Output Low Voltage vs. Ambient Temperature

#### TYPICAL PERFORMANCE CHARACTERISTICS (continued)

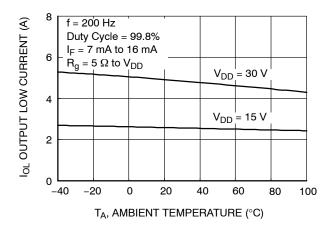


Figure 7. Output Low Current vs. Ambient Temperature

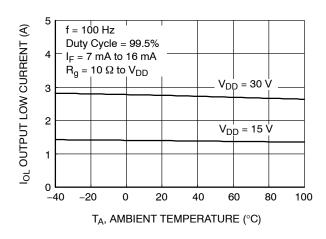


Figure 8. Output Low Current vs. Ambient Temperature

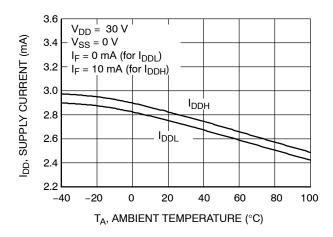


Figure 9. Supply Current vs. Ambient Temperature

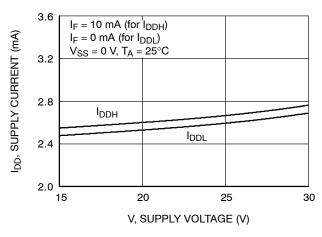


Figure 10. Supply Current vs. Supply Voltage

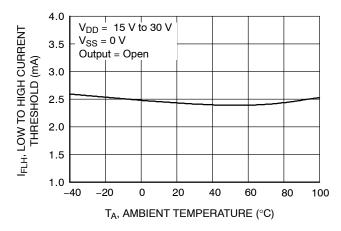


Figure 11. Low to High Input Current Threshold vs. Ambient Temperature

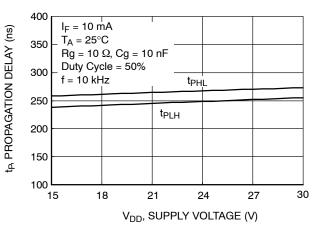


Figure 12. Propagation Delay vs. Supply Voltage

#### TYPICAL PERFORMANCE CHARACTERISTICS (continued)

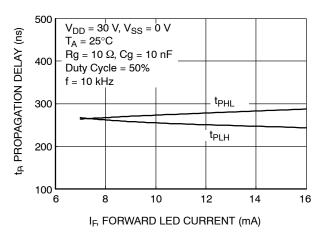


Figure 13. Propagation Delay vs. LED Forward
Current

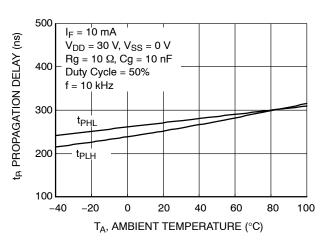


Figure 14. Propagation Delay vs. Ambient Temperature

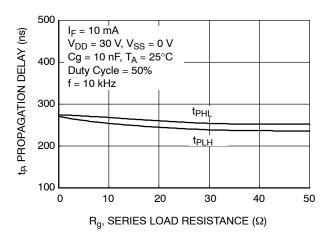


Figure 15. Propagation Delay vs. Series Load Resistance

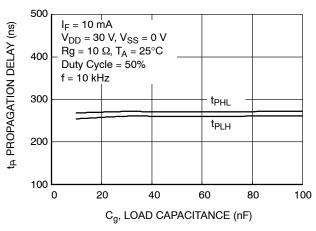


Figure 16. Propagation Delay vs. Load Capacitance

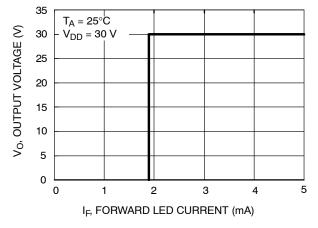


Figure 17. Transfer Characteristics

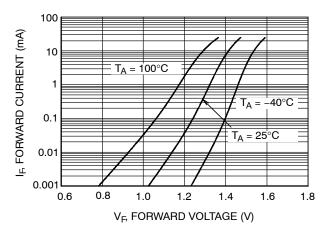


Figure 18. Input Forward Current vs. Forward Voltage

## TYPICAL PERFORMANCE CHARACTERISTICS (continued)

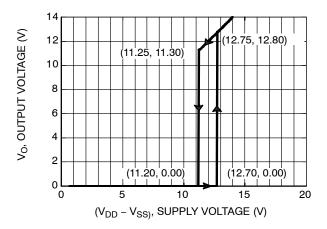


Figure 19. Under Voltage Lockout

## **TEST CIRCUIT**

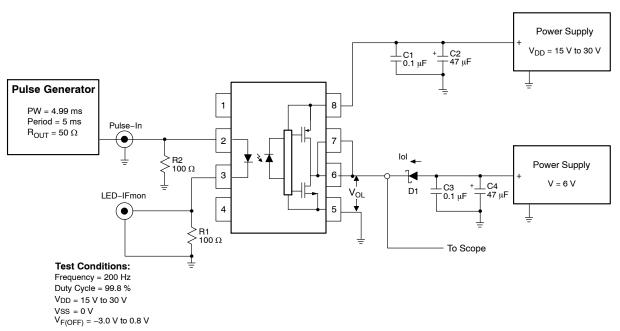


Figure 20. I<sub>OL</sub> Test Circuit

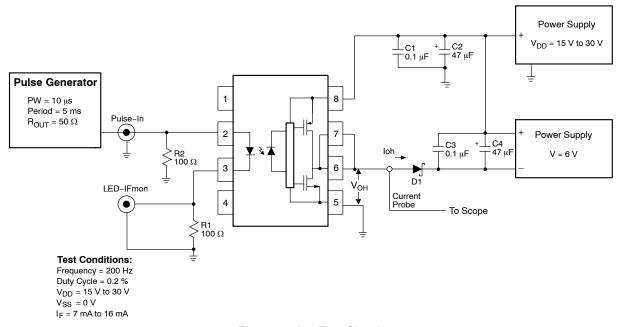


Figure 21. I<sub>OH</sub> Test Circuit

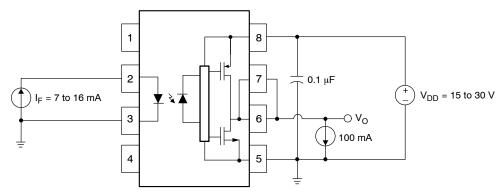


Figure 22. V<sub>OH</sub> Test Circuit

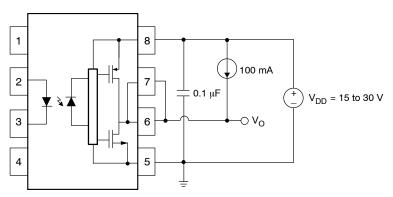


Figure 23. V<sub>OL</sub> Test Circuit

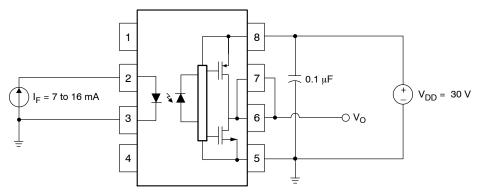


Figure 24. I<sub>DDH</sub> Test Circuit

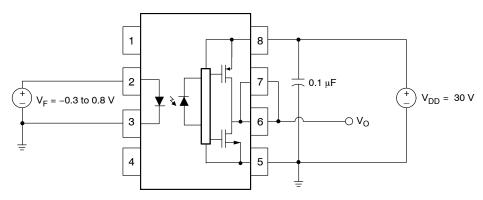


Figure 25.  $I_{DDL}$  Test Circuit

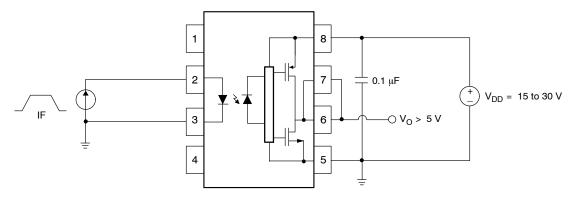


Figure 26. I<sub>FLH</sub> Test Circuit

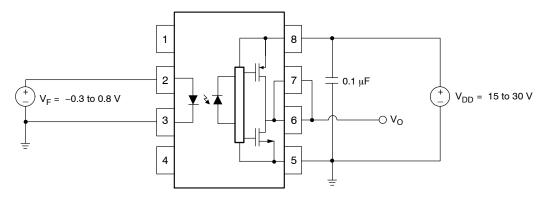


Figure 27. V<sub>FHL</sub> Test Circuit

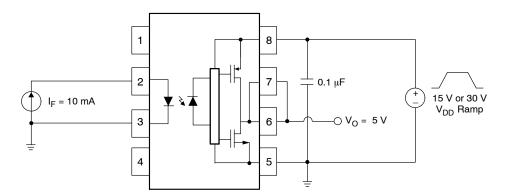


Figure 28. UVLO Test Circuit

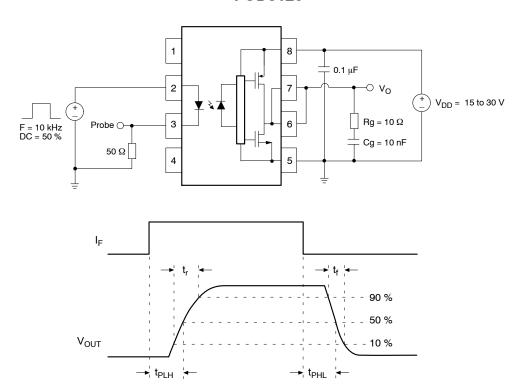


Figure 29.  $t_{\text{PHL}}$ ,  $t_{\text{PLH}}$ ,  $t_{\text{R}}$  and  $t_{\text{F}}$  Test Circuit and Waveforms

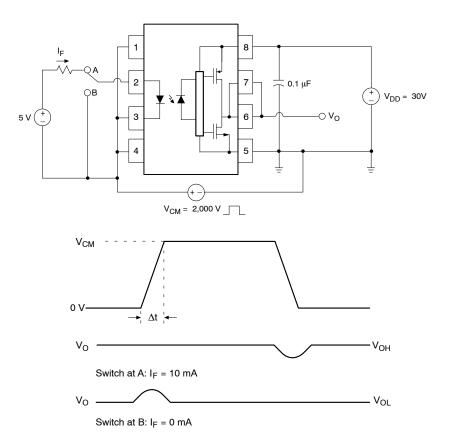


Figure 30. CMR Test Circuit and Waveforms

## **REFLOW PROFILE**

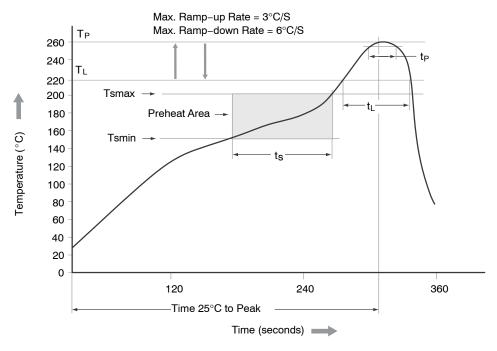


Figure 31. Reflow Profile

**Table 9. REFLOW PROFILE** 

Profile Feature	Pb-Free Assembly Profile
Temperature Min. (Tsmin)	150°C
Temperature Max. (Tsmax)	200°C
Time (t <sub>S</sub> ) from (Tsmin to Tsmax)	60-120 s
Ramp-up Rate (t <sub>L</sub> to t <sub>P</sub> )	3°C/s max.
Liquidous Temperature (T <sub>L</sub> )	217°C
Time (t <sub>L</sub> ) Maintained Above (T <sub>L</sub> )	60–150 s
Peak Body Package Temperature	260°C +0°C / -5°C
Time (t <sub>P</sub> ) within 5°C of 260°C	30 s
Ramp-down Rate (T <sub>P</sub> to T <sub>L</sub> )	6°C/s max.
Time 25°C to Peak Temperature	8 min. max.

#### **ORDERING INFORMATION**

Part Number	Package	Shipping <sup>†</sup>
FOD3120	DIP 8-Pin	50 / Tube
FOD3120S	SMT 8-Pin (Lead Bend)	50 / Tube
FOD3120SD	SMT 8-Pin (Lead Bend)	1000 / Tape & Reel
FOD3120V	DIP 8-Pin, DIN EN/IEC60747-5-5 option	50 / Tube
FOD3120SV	SMT 8-Pin (Lead Bend), DIN EN/IEC60747-5-5 option	50 / Tube
FOD3120SDV	SMT 8-Pin (Lead Bend), DIN EN/IEC60747-5-5 option	1000 / Tape & Reel
FOD3120TV	DIP 8-Pin, 0.4" Lead Spacing, DIN EN/IEC60747-5-5 option	50 / Tube
FOD3120TSV	SMT 8-Pin, 0.4" Lead Spacing, DIN EN/IEC60747-5-5 option	50 / Tube
FOD3120TSR2V	SMT 8-Pin, 0.4" Lead Spacing, DIN EN/IEC60747-5-5 option	700 / Tape & Reel

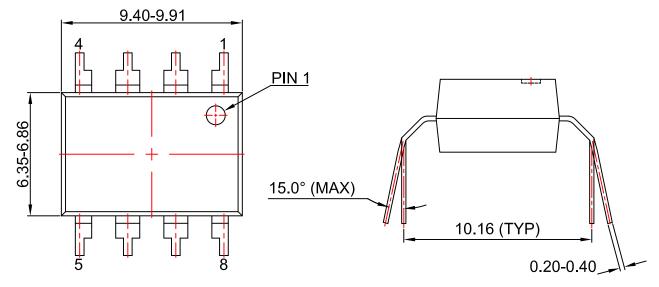
<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D

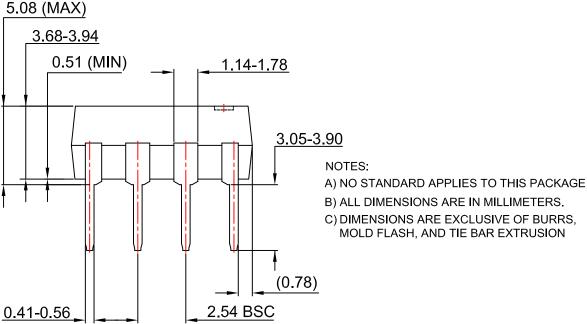
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#### PDIP8 6.6x3.81, 2.54P CASE 646BW ISSUE O

**DATE 31 JUL 2016** 



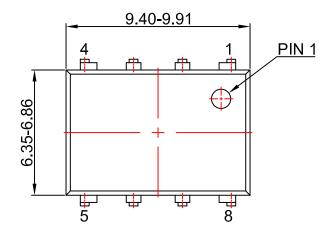


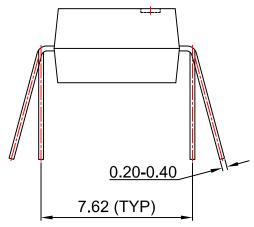
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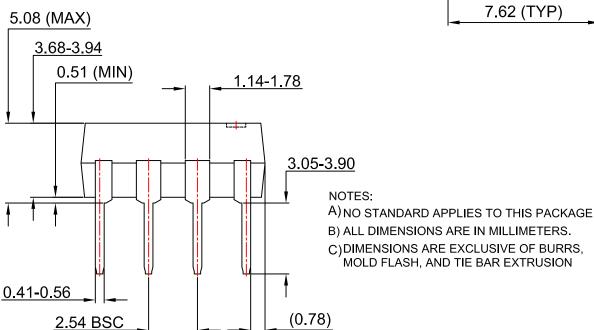


#### PDIP8 9.655x6.6, 2.54P CASE 646CQ ISSUE O

**DATE 18 SEP 2017** 





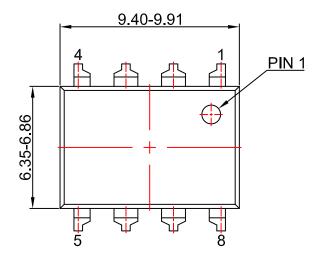


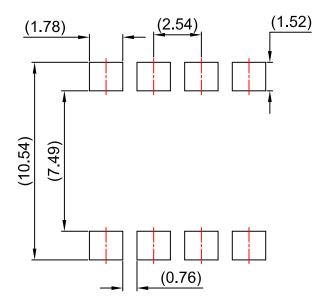
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PDIP8 GW CASE 709AC ISSUE O

**DATE 31 JUL 2016** 





5.08 (MAX)

3.68-3.94

0.51 (MIN)

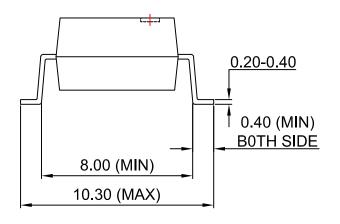
1.14-1.78

(0.78)

2.54BSC

0.41-0.56





#### NOTES:

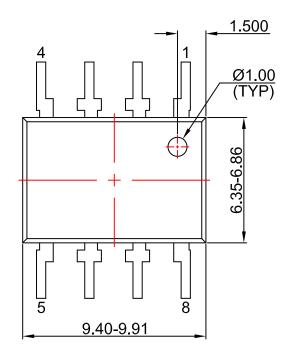
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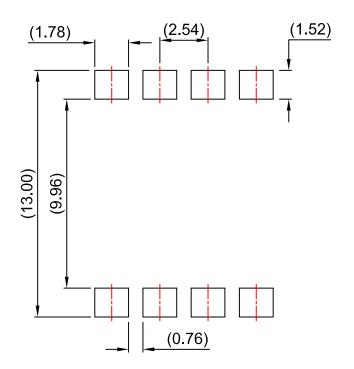
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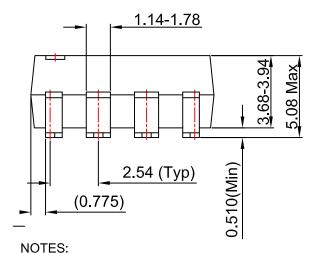
PDIP8 GW CASE 709AD ISSUE O

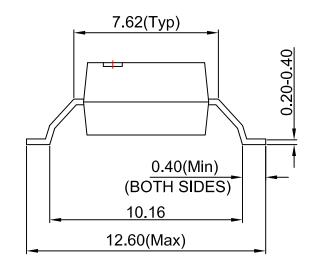
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LAND PATTERN RECOMMENDATION





#### .....

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