# **MPN3404**

# **Silicon Pin Diode**

This device is designed primarily for VHF band switching applications but is also suitable for use in general–purpose switching circuits. It is supplied in a cost–effective TO–92 type plastic package for economical, high–volume consumer and industrial requirements.

#### **Features**

- Rugged PIN Structure Coupled with Wirebond Construction for Optimum Reliability
- Low Series Resistance @ 100 MHz:  $R_S = 0.7 \Omega$  (Typ) @  $I_F = 10 \text{ mAdc}$
- Sturdy TO-92 Style Package for Handling Ease
- Pb-Free Packages are Available\*



Rating	Symbol	Value	Unit
Reverse Voltage	V <sub>R</sub>	20	Vdc
Forward Power Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	400 4.0	mW mW/°C
Junction Temperature	TJ	+125	°C
Storage Temperature Range	T <sub>stg</sub>	-55 to +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Breakdown Voltage (I <sub>R</sub> = 10 μAdc)	V <sub>(BR)R</sub>	20	-	-	Vdc
Diode Capacitance (V <sub>R</sub> = 15 Vdc, f = 1.0 MHz)	C <sub>T</sub>	-	1.3	2.0	pF
Series Resistance (Figure 5) (I <sub>F</sub> = 10 mAdc)	R <sub>S</sub>	-	0.7	0.85	Ω
Reverse Leakage Current (V <sub>R</sub> = 15 Vdc)	I <sub>R</sub>	-	-	0.1	μAdc



# ON Semiconductor®

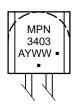
http://onsemi.com





TO-92 (TO-226AC) CASE 182-06 STYLE 1

### **MARKING DIAGRAM**



A = Assembly Location

′ = Year

WW = Work Week

= Pb-Free Package

(Note: Microdot may be in either location)

#### ORDERING INFORMATION

Device	Package	Shipping
MPN3404	TO-92	1000 Units / Bulk
MPN3404G	TO-92 (Pb-Free)	1000 Units / Bulk

<sup>\*</sup>For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## **MPN3404**

# **TYPICAL CHARACTERISTICS**

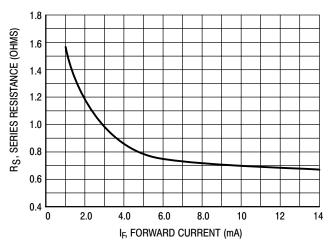
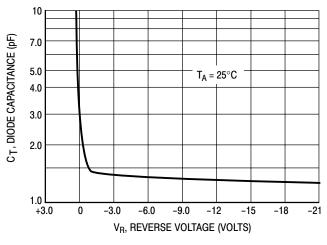


Figure 1. Series Resistance

Figure 2. Forward Voltage



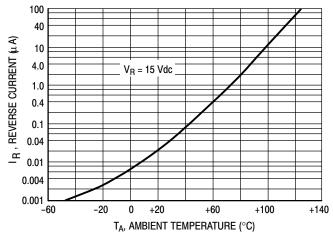


Figure 3. Diode Capacitance

Figure 4. Leakage Current

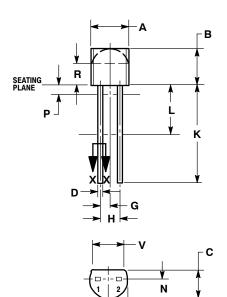




TO-92 (TO-226) CASE 182-06 **ISSUE L** 

**DATE 04/18/1998** 







**SECTION X-X** 

- OTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. CONTOUR OF PACKAGE BEYOND ZONE R IS UNCONTROLLED.
  4. LEAD DIMENSION IS UNCONTROLLED IN P AND

- BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETER		
DIM	MIN	MAX	MIN	MAX	
Α	0.175	0.205	4.45	5.21	
В	0.170	0.210	4.32	5.33	
C	0.125	0.165	3.18	4.19	
D	0.016	0.021	0.407	0.533	
G	0.050 BSC		1.27 BSC		
Н	0.100 BSC		2.54 BSC		
ſ	0.014	0.016	0.36	0.41	
K	0.500		12.70		
٦	0.250		6.35		
N	0.080	0.105	2.03	2.66	
Р		0.050		1.27	
R	0.115		2.93		
٧	0.135		3.43		

STYLE 1: PIN 1. ANODE 2. CATHODE STYLE 2: PIN 1. CATHODE 2. ANODE

STYLE 3: PIN 1. MAIN TERMINAL 1 2. MAIN TERMINAL 2 STYLE 4: CANCELLED STYLE 5: PIN 1. INPUT 2. OUTPUT

DOCUMENT NUMBER:	98ASB42118B	Electronic versions are uncontrolled except when accessed directly from the Document Repository Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.		
DESCRIPTION:	TO-92 (TO-226)		PAGE 1 OF 1	

onsemi and ONSEMi are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

onsemi, Onsemi, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at <a href="www.onsemi.com/site/pdf/Patent-Marking.pdf">www.onsemi.com/site/pdf/Patent-Marking.pdf</a>. Onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA class 3 medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase

#### ADDITIONAL INFORMATION

**TECHNICAL PUBLICATIONS:** 

 $\textbf{Technical Library:} \ \underline{www.onsemi.com/design/resources/technical-documentation}$ 

onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at

www.onsemi.com/support/sales