

Automotive PLED Series (PLEDxS-A)

🚘 AUTOMOTIVE GRADE



Agency Approvals

Agency	Agency File Number
91	E133083

Schematic Symbol



Description

Automotive PLED Series (PLEDxS-A) open LED protectors provide a switching electronic shunt path when an LED in an LED string fails as an open circuit. This ensures that the remaining string of LEDs will continue to function if a single LED does not.

This series is designed for automotive applications such as automotive car head lamp, tail lamp, LED indicator protection, aircraft runway lighting and other applications need high reliability requirements.

Compatible with one, two and three watt LEDs that have a nominal 3V forward characteristic.

Features

- Recognized to UL 497B as an Isolated Loop Circuit Protector
- AEC–Q101 Qualified and PPAP Capable
- Fast switching
- Automatically resets after power cycle
- Available in standard DO-214AA package

- Compatible with industrial lighting environments
- IEC-61000-4-2 ESD 30kV (Air), 30kV (Contact)
- ESD protection of data lines in accordance with IEC 61000-4-2 (IEC801-2)
- Compatible with PWM frequencies up to 30 kHz
- RoHS compliant and halogen-free

Electrical Characteristics (All parameters are measured at T_A=25°C unless otherwise noted)

Part Number	Marking	V _{DRM} @I _{DRM} =5µA Volts	V _s @100V/µs Volts	I _H mAmps	l _s mAmps	I _⊤ @V _⊤ Amps	V _T @ I _T = 1 Amp Volts	Critical rate of rise dV/dt Volts
		Min	Max	Min	Max	Max	Max	Max
PLED6S-A	AL6	6	27	5	100	1.0	1.2	250V/µs
PLED9S-A	AL9	9	30	5	100	1.0	1.2	250V/µs
PLED13S-A	AL13	13	44	5	100	1.0	1.2	250V/µs
PLED18S-A	AL18	18	55	5	100	1.0	1.2	250V/µs



Thermal Considerations

Symbol	Parameter	Value	Unit
TJ	Operating Junction Temperature Range	-55 to +150	°C
T _s	Storage Temperature Range	-65 to +150	°C
R _{eja}	Thermal Resistance: Junction to Ambient	DO-214AA: 125 ¹ DO-214AA: 40 ²	°C/W

Notes:

1) Standard FR-4 PCB with Copper Pads (Recommended Size)

2) Aluminium PCB

Thickness: 1.6mm

Grade: 1-2 W/mK Thermal Conductivity Trace thickness: 2 oz

Insulation layer thickness: 215 µm

Solder Pad Dimensions: 2.0mm x 2.8mm (Recommended Size)

V-I Characteristics



Normalized VS Change vs. Junction Temperature



Normalized DC Holding Current vs. Case Temperature



LED Interference Test Circuit





6 LEDs in Series 50% Duty Cycle 10kHz

5 LEDs and 1 PLED in Series 50% Duty Cycle 10kHz



Note: These two graphs show the current magnitude through the LED string with and without the PLED included. There is no noticeable effect on the LED current magnitude when the PLED is included in the circuit as compared to the LED current magnitude when the PLED is not in the circuit. (The conversion factor for the test measurement in the graphs above is 10mA/mV for the Pearson coil measurement, therefore, the current magnitude in the first figure is 10mA*8.9 = 89mA, while the second figure is 91mA.)



Channel 1: current through LEDs (318 mA) Channel 2: voltage across PLED component (4.5 V)

PLED component zeners and then turns fully on 10kHz



Channel 1: current through LEDs (346 mA) and PLED component once it is fully turned on 2.5 μ sec later Channel 2: voltage across PLED component (21.3 V before PLED crowbars with 2 V drop)



Soldering Parameters

Reflow Co	ndition	Pb – Free assembly	
	-Temperature Min (T _{s(min)})	150°C	
Pre Heat	-Temperature Max (T _{s(max)})	200°C	
	-Time (min to max) (t _s)	60 – 180 secs	
Average ramp up rate (LiquidusTemp (T _L) to peak		3°C/second max	
$T_{S(max)}$ to T_L	- Ramp-up Rate	3°C/second max	
D (I	-Temperature (T _L) (Liquidus)	217°C	
Reflow	-Temperature (t _L)	60 – 150 seconds	
PeakTemperature (T _P)		260 ^{+0/-5} °C	
Time within 5°C of actual peak Temperature (t _p)		30 seconds	
Ramp-down Rate		6°C/second max	
Time 25°C to peak Temperature (T _P)		8 minutes max	
Do not exceed		260°C	



Environmental Specifications

High Temp Voltage Blocking	80% Rated V_{DRM} (V_{DC} Peak) +150°C, 1008 hrs. MIL-STD-750 (Method 1040) JEDEC, JESD22-A-101		
Temp Cycling	-55°C to +150°C, 15 min. dwell, 1000 cycles. MIL-STD-750 (Method 1051) EIA/JEDEC, JESD22-A104		
Biased Temp & Humidity	80% Rated V _{DRM} (+85°C) 85% RH, 504 up to 1008 hrs. EIA/JEDEC, JESD22-A-101		
Unbiased Highly Accelerated Stress Test	+130°C,85%RH,2atm,96hrs.JESD22A-118		
Resistance to Solder Heat	+260°C, 10 secs. MIL-STD-750 (Method 2031)		
Moisture Sensitivity Level	85%RH, +85°C, 168 hrs., 3 reflow cycles (+260°C Peak). JEDEC-J-STD-020, Level 1		

Physical Specifications				
Terminal Material	Copper Alloy			
Terminal Finish	100% Matte Tin Plated			
Body Material	UL recognized compound meeting flammability classification V-0			

Part Numbering System



Part Marking System





PLED Open LED Protectors

Automotive PLED Series

Packaging

Package	Description	Packaging Quantity	Industry Standard
S	D O - 2 1 4 A A	2500	EIA-481-1

Dimensions - DO-214 AA Package





(Reference Only)

Dimensions	Inc	hes	Millimeters		
DIMENSIONS	Min	Max	Min	Max	
А	0.130	0.156	3.30	3.95	
В	0.201	0.220	5.10	5.60	
С	0.077	0.087	1.95	2.20	
D	0.159	0.181	4.05	4.60	
E	0.030	0.063	0.75	1.60	
F	0.075	0.096	1.90	2.45	
G	0.002	0.008	0.05	0.20	
Н	0.077	0.104	1.95	2.65	
K	0.006	0.016	0.15	0.41	

DO-214AA Embossed Carrier Reel Pack (RP)



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