

Features

6.90

(0.272)

PIN 1

- IEEE 802.3 Ethernet compatible
- Designed for AMD IC manufacturers
- Fully integrated for adapter, hub and motherboard applications
- Extended temperature range: -40 °C to +85 °C
- **RoHS** compliant*

Product Dimensions

PT61017XPEL LAN 10/100 Base-Tx

12.80 ± 0.25 (0.504 ± 0.010)

Electrical Specifications @ 25 °C Turns Ratio ± 5%.....1CT*:1CT* Inductance Pin 1-3, 6-8 ...350 µH min. @ 100 KHz, 0.2 Vrms, 8 mA DC Bias DCR Pin 1-3, 6-8 0.6 Ω max. Pin 9-11, 14-16..... 1.1 Ω max. Insertion Loss 0.3-100 MHz -1.15 dB max. Return Loss 1-40 MHz-16 dB min. 40-100 MHz--10+20*LOG10(f/80 MHz) dB min. Common Mode Rejection 30 MHz.....-40 dB Min. 60 MHz.....-35 dB Min. 100 MHz.....-30 dB Min. Crosstalk 1-60 MHz-45 dB Min. 60-100 MHz-35 dB Min. Hipot 1 mA, 2 sec. 1500 VAC (PRI-SEC) **Operating Temperature**-40 °C to +85 °C Storage Temperature.. -40 °C to +125 °C

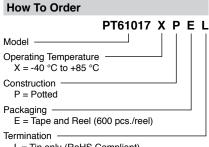
*CT: Center tap

Material

Termination	Tin
Iermination	IIn

Packaging Specifications

Tape & Reel..... 600 pcs./reel



L = Tin only (RoHS Compliant)



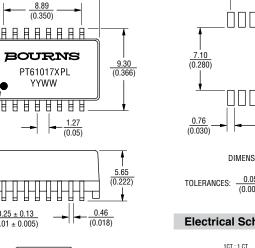
REV. 08/19

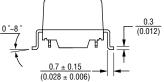
*RoHS Directive 2015/863, Mar 31, 2015 and Annex.

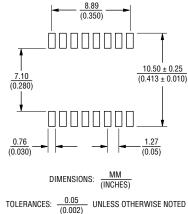
Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

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Recommended Layout

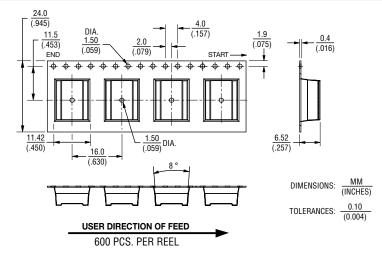
Electrical Schematic

TD+ CT TD-	1 2 3	-0	15	TX+ CT TX-
RD+ CT RD-	7	-0	11 10 9	RX+ CT RX-

Packaging Specifications

 0.25 ± 0.13

 (0.01 ± 0.005)



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